



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

KIMURA et al

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Appl. No. 10/668,166

TC/A.U. 2811

Filed: September 24, 2003

Examiner: Arena, Andrew O.

For: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE

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January 29, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE AFTER FINAL

Responsive to the Official Action dated October 27, 2006, reconsideration is respectfully requested for at least the following reasons.

Approved For Entry
A.D.A.
4 February 2007